

Reports:

RTP 1103060 R003 IEC 61508 Functional Safety Assessment Report V1 R1 RTP 1103060 R004 Security Assessment Report V1 R1

Validity:

This assessment is valid for RTP3000 Dual, Triple and Quad systems with Node Processor 3000/02, Firmware A4.36 or later.

This assessment is valid until September 1, 2014.

Revision 1.1 August 12, 2011



Certificate / Certificat Zertifikat / 合格証

RTP 1103060 C001

exida hereby confirms that the:

RTP 3000

Manufactured by:

RTP Corporation Pompano Beach, FL USA

Has been assessed per the relevant requirements of:

IEC 61508: 2010 Parts 1-7

and meets requirements providing a level of integrity to:

IEC 61508: Systematic Integrity-SIL 3 Capable Random Integrity: Type B Element and

ISASecure[™] Embedded Device Security Assurance 2010.1

and meets requirements providing a level of integrity to:

Level 2

Safety Function:

The RTP-3000 reads inputs, performs its programmed safety function, and generates outputs.

Application Restrictions:

The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements. The unit must be operated in a network and operational environment per the Security Manual requirements.



Certificate / Certificat / Zertifikat / 合格証

RTP 1103060 C001

Systematic Integrity: SIL 3 Capable
Random Integrity for Type B Device:
SIL 3 @ HFT=0
Security Integrity: Level 2

RTP 3000 RTP Corporation Pompano Beach, FL

SIL 3 Capability:

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated without "prior use" justification by end user or diverse technology redundancy in the design.

Security Integrity:

The product has met the requirements of EDSA-300 for Level 2.

IEC 61508 Failure Rates in FIT* are Available from RTP Corporation



Form Version Date
CFI 1.1 August 2011

SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of PFD_{AVG} considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each subsystem must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

* FIT = 1 failure / 109 hours